IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Werner Juengling	Examiner:	Lisa Kilday
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	303.510US2
Title:	METHODS FOR MAKING NEARLY PLANAR DIELECTRIC FILMS IN INTEGRATED CIRCUITS		

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Pursuant to 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicant's prior U.S. application, Serial No. 09/801265, filed on March 07, 2001, which is relied upon for an earlier filing date under 35 U.S.C. §120.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

WERNER JUENGLING

By his Representatives,

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Date 30 Sept 2003 By _____
Eduardo E. Drake
Reg. No. 40,594

"Express Mail" mailing label number: EV298566362US
Date of Deposit: September 30, 2003
This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.

PTO/SB/08A(10-01)
Approved for use through 10/31/2002. OMB 651-0031
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known	
Application Number	Unknown
Filing Date	Even Date Herewith
First Named Inventor	Juengling, Werner
Group Art Unit	Unknown
Examiner Name	Kilday, Lisa

Sheet 1 of 1

Attorney Docket No: 303.510US2

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,518,959	05/21/1996	Jang, S. , et al.	437	190	08/24/1995
	US-5,776,834	07/07/1998	Avanzino, S. , et al.	438	692	06/07/1995
	US-5,814,555	09/29/1998	Bandyopadhyay, B. , et al.	438	619	06/05/1996
	US-5,858,876	01/12/1999	Chew, P.	438	695	04/01/1996
	US-5,872,058	02/16/1999	Van Cleemput, P. A., et al.	438	692	06/17/1997
	US-6,030,881	02/29/2000	Papsouliotis, G. D., et al.	438	424	05/05/1998
	US-6,207,571	03/27/2001	Juengling, W. , et al.	438	692	03/01/1900
	US-6,223,331	04/24/2001	Juengling, W.	716	10	07/30/1998

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T[2]
	EP-0766291	04/02/1997	Nag, S. S., et al.	H01L	21/314	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No [1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
		"Chemical Vapor Deposition", Trikon Technologies, http://www.trikon.com/trikon/html/cvd.html,(1999),pp. 1-5	
		STINE, B. E., et al., "The Physical and Electrical Effects of Metal-Fill Patterning Practices for Oxide Chemical-Mechanical Polishing Processes", IEEE Transactions on Electron Devices, 45(3), (1998),pp . 665-679	
		WOLF, S. , Silicon Processing for the VLSI Era, Volume 2: Process Integration, Lattice Press, Sunset Beach, California, ISBN 0-961672-4-5,(1990),pp. 176-297	

EXAMINER **DATE CONSIDERED**

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached